



TIME TABLE FOR M.E./M.Tech. DEGREE EXAMINATIONS - NOV./DEC-2016

for Candidates admitted in erstwhile Anna University of Technology, Madurai in the year 2010

Regulations : 2010

Branch:M.E-Applied Electronics(PT)

I Semester	Session :	10 A.M. to 1 P.M.
Exam Date	Day	Sub.Code/Name
16/11/2016	Wednesday	10277AM101:Applied Mathematics

Branch:M.E-VLSI Design(PT)

I Semester	Session :	10 A.M. to 1 P.M.
Exam Date	Day	Sub.Code/Name
15/11/2016	Tuesday	10244VL105:Solid State Device Modeling and Simulation
II Semester	Session :	10 A.M. to 1 P.M.
Exam Date	Day	Sub.Code/Name
22/11/2016	Tuesday	10244VL203:Computer Aided Design of VLSI Circuits
Electives	Session :	10 A.M. to 1 P.M.
Exam Date	Day	Sub.Code/Name
17/11/2016	Thursday	10244CME22:DSP Processor Architecture and programming
28/11/2016	Monday	10244VLE51:Low Power VLSI Design
02/12/2016	Friday	10244VLE41:Physical Design of VLSI Circuits
03/12/2016	Saturday	10244VLE22:Testing of VLSI Circuits
05/12/2016	Monday	10244VLE32:VLSI Technology