

UNIVERSITY OF KERALA**TIME TABLE FOR THE SECOND SEMESTER B.Sc. ELECTRONICS
DEGREE EXAMINATION JULY 2017**

CAREER RELATED FIRST DEGREE PROGRAMME UNDER CBCSS- GROUP 2 (b)
2013 and 2014 Admission – Supplementary (2013 admission onwards)
2016 admission – Regular only – 2015 admin. Improvement and Supplementary – (2015
admission onwards)

Time : 1.30 pm to 4.30 pm on all days
(Except Friday) 2.00 pm to 5.00 pm on Friday

DAY & DATE	COURSE	COURSE CODE	COURSE TITLE
Tuesday 25.07.2017	Language Course II	EN 1211.4	English II – Writing & Presentation Skills (2013 Admn. Onwards) Career related First Degree Programme (CBCSS) Group 2 (b) Common for B.Voc. Degree Programme in Software Development (EN 1211) & For B.Voc. Degree Programme in Tourism and Hospitality Management (EN 211)
Thursday 27.07.2017	Foundation Course	MM 1221.8	Mathematics II (2013 Admn. Onwards)
Tuesday 01.08.2017	Core Course	EX 1241	Solid State Electronics (2015 Admn. Onwards) Electronic circuits (2013 & 2014 Admn.)
Thursday 03.08.2017	Core Course	EX 1242	Network Analysis (2015 Admn. Onwards) Digital Electronics (2013 & 2014 Admn.)
Monday 07.08.2017	Core Course	EX 1243	Signals & System (2013 & 2014 Admn.)
Wednesday 09.08.2017	Complementary Course	EX 1231	Digital Electronics (2015 Admn. Onwards) Computer Technology (2013 & 2014 Admn.)

CONTROLLER OF EXAMINATIONS

Thiruvananthapuram

Dated: 18.07.2017